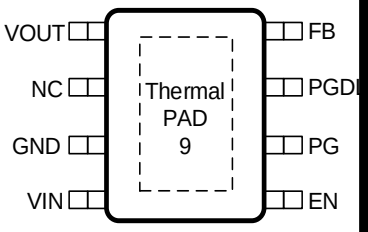
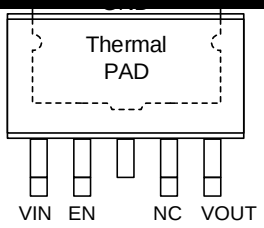


SCT71405A00STER



SCT71405FxxOWCR

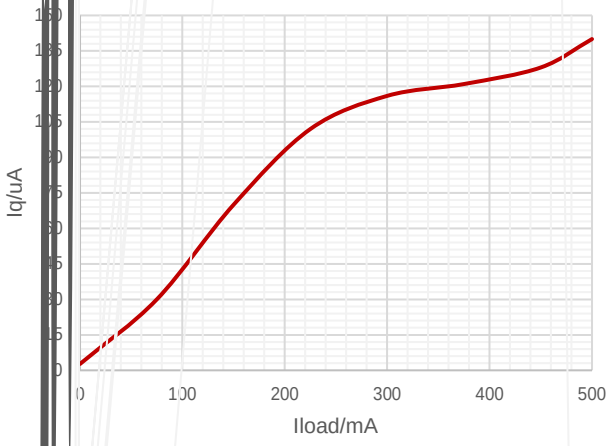
NAME	PIN	
	ESOP-8	ESOP-8

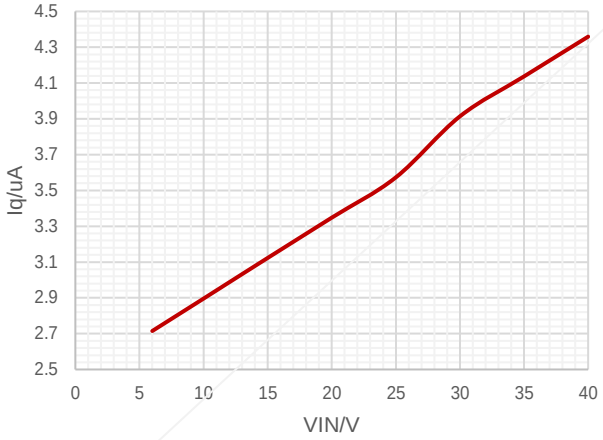
NAME	PIN FUNCTION		
	TO252-4	TO252-4	
	1		

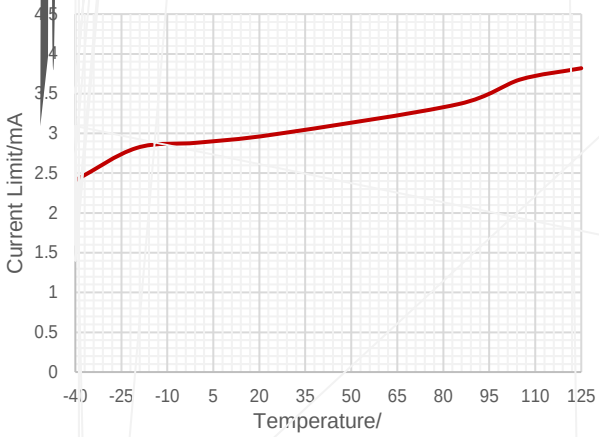
1

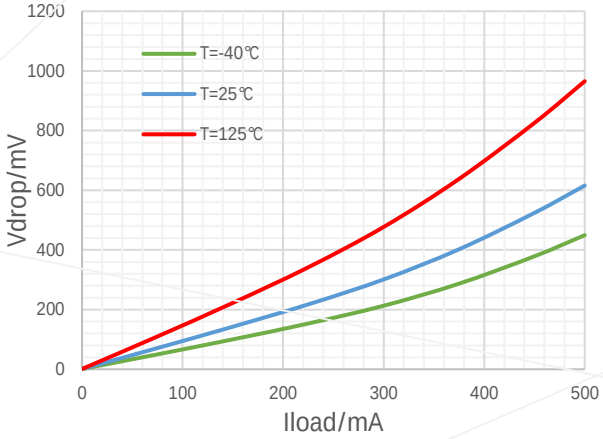
PARAMETER（4-layer）

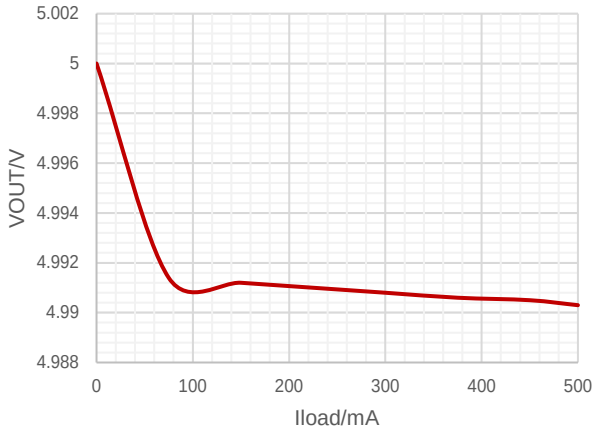
SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply						
	( ) ( )					

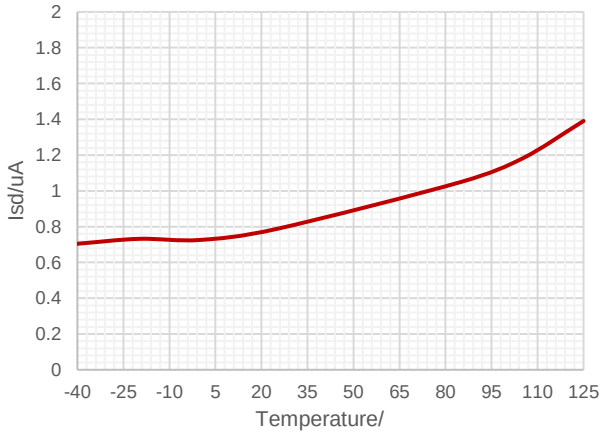

Iq/uA
Iload/mA

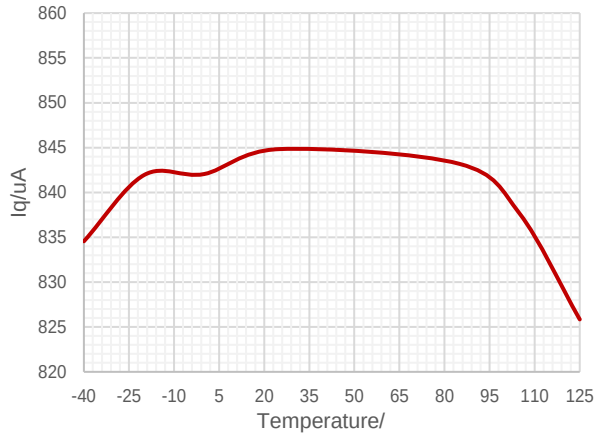

Iq/uA
VIN/V

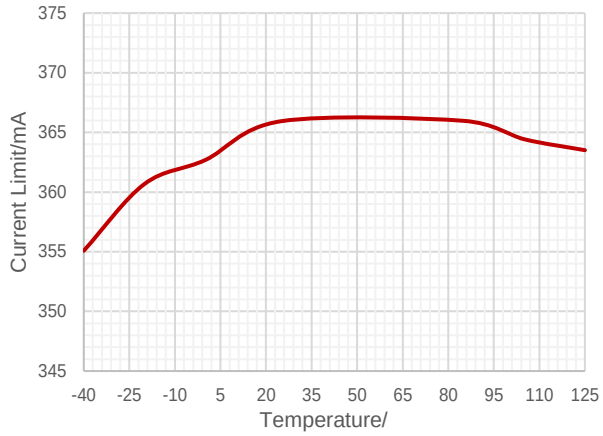

Current Limit/mA
Temperature/


T=-40℃
T=25℃
T=125℃
Vdrop/mV
Iload/mA

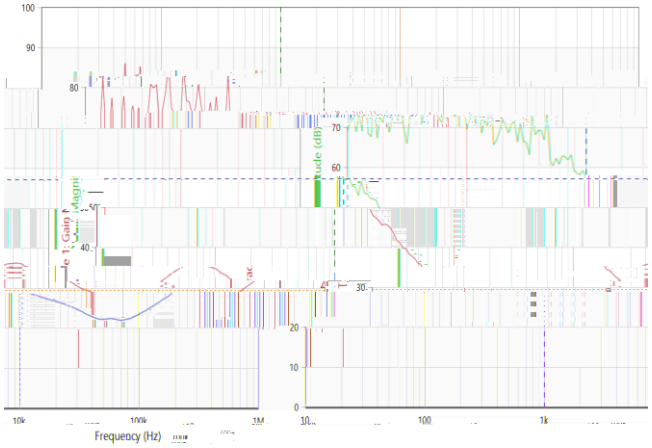

5.002
5
4.998
4.996
4.994
4.992
4.99
4.988
VOUT/V
0
100
200
300
400
500
Iload/mA

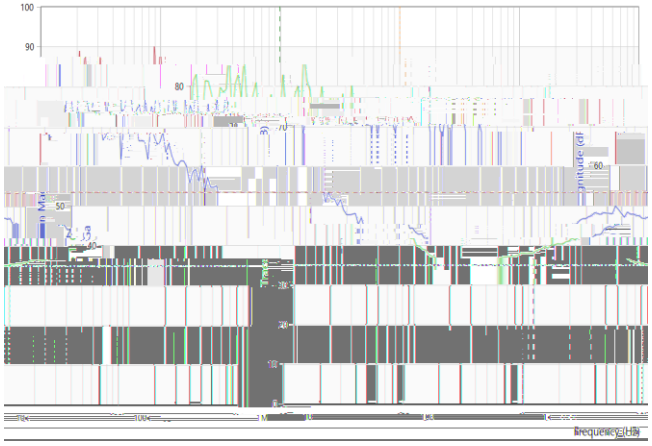

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
Isd/uA
-40
-25
-10
5
20
35
50
65
80
95
110
125
Temperature/

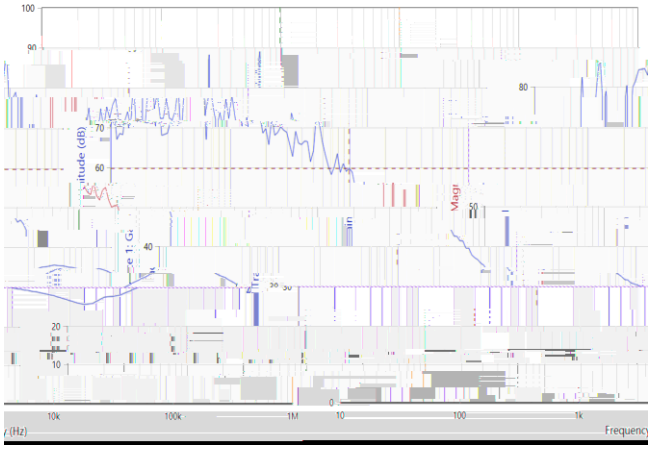

860
855
850
845
840
835
830
825
820
Iq/uA
-40
-25
-10
5
20
35
50
65
80
95
110
125
Temperature/

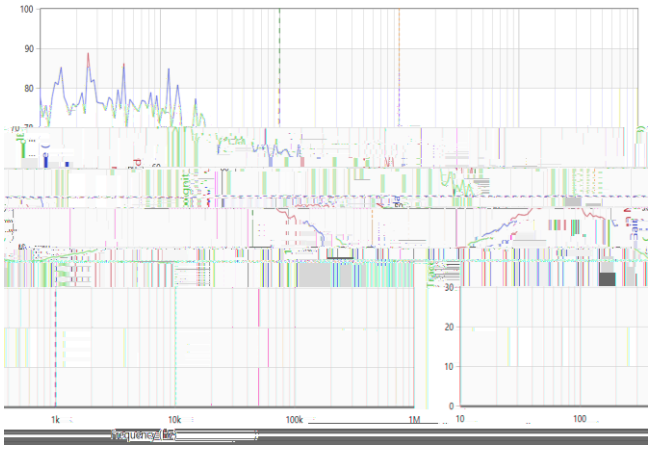

375
370
365
360
355
350
345
Current Limit/mA
-40
-25
-10
5
20
35
50
65
80
95
110
125
Temperature/

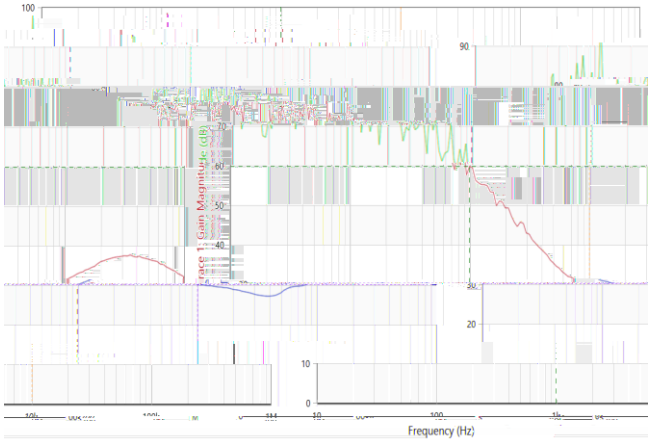
≥

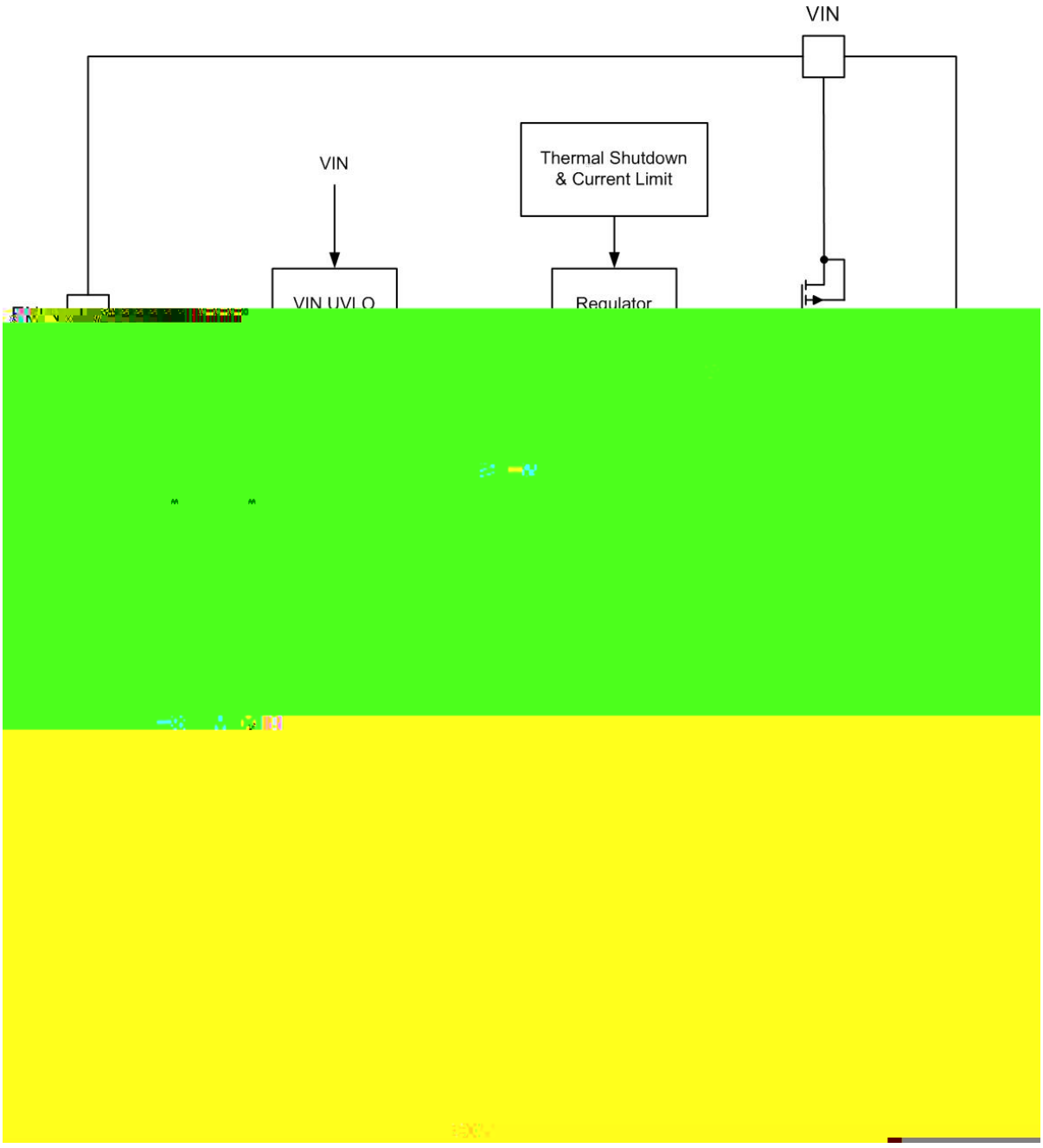

Frequency (Hz)





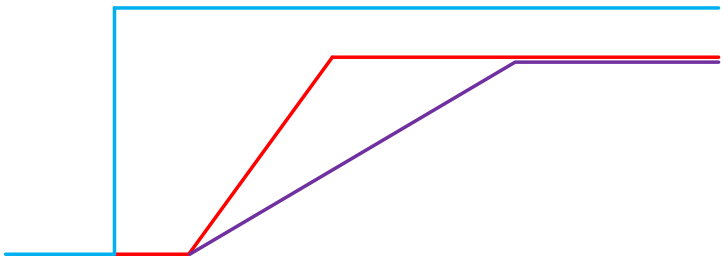



Frequency (Hz)


VIN
VIN
Thermal Shutdown
& Current Limit
VIN UVLO
Regulator

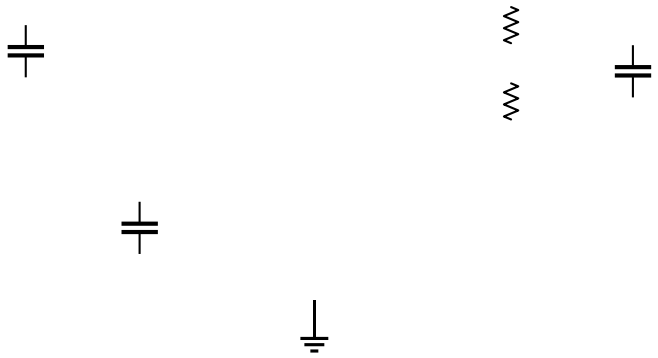
Overview

Over Current Limit and Foldback Current Limit



Power-Good and Power-Good Delay

Typical application 1:



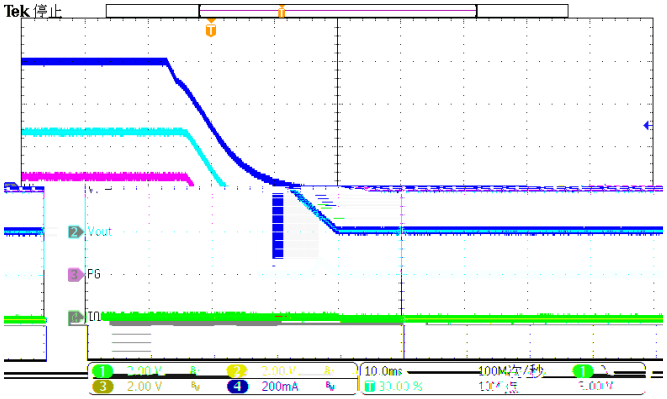
SCT71405 Series

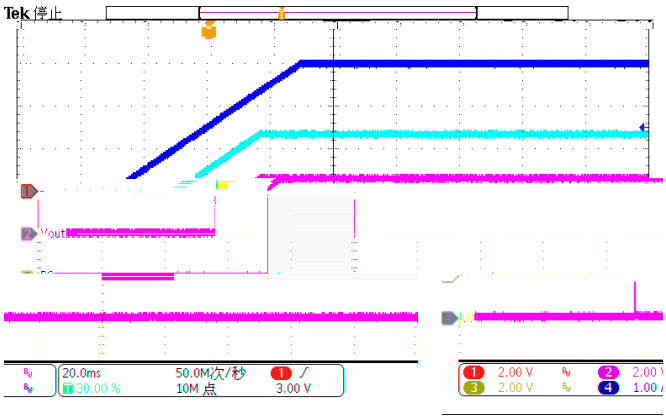
Output Voltage

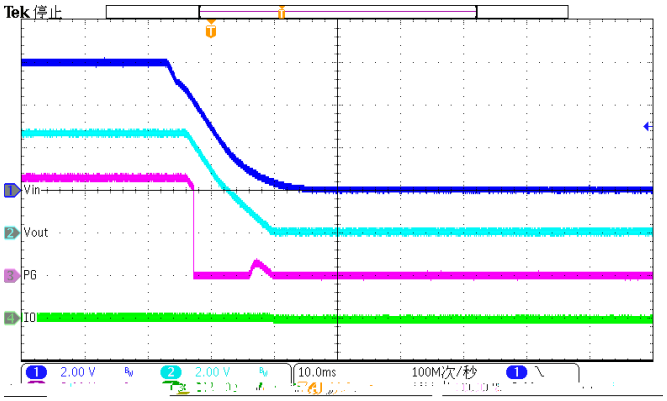
Table 1: Compensation Values for Typical Output Voltage/Capacitor Combinations

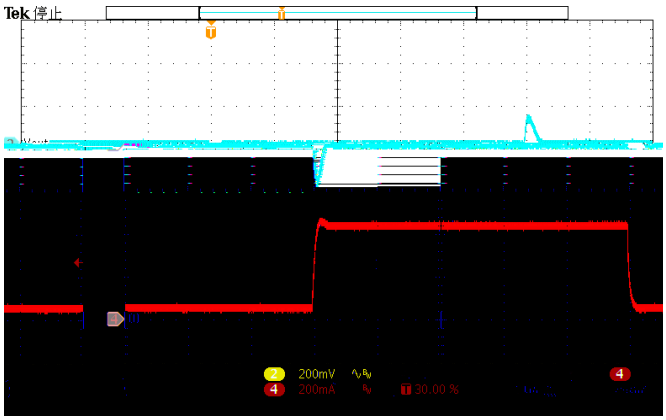
Power Dissipation and Thermal Performance

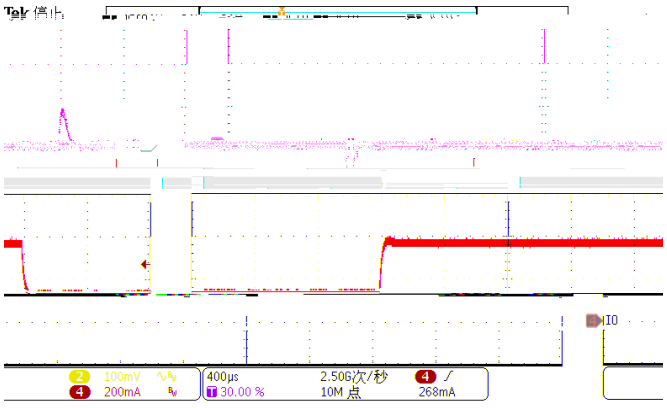
Application Waveforms



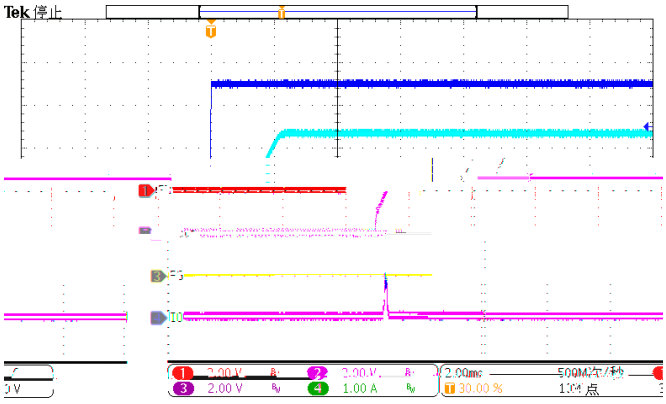


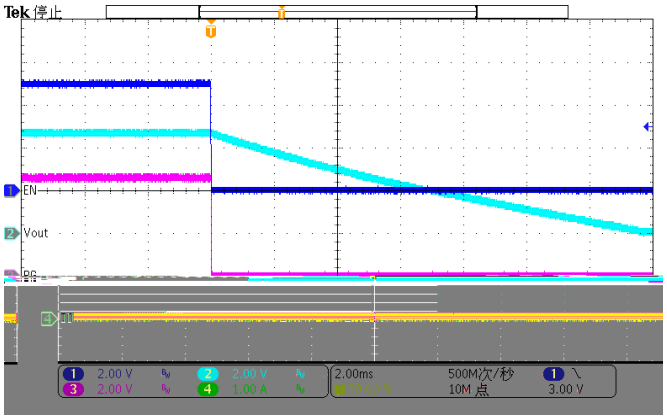


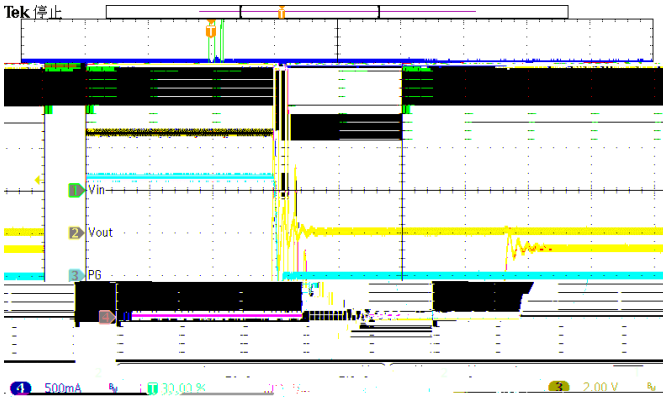


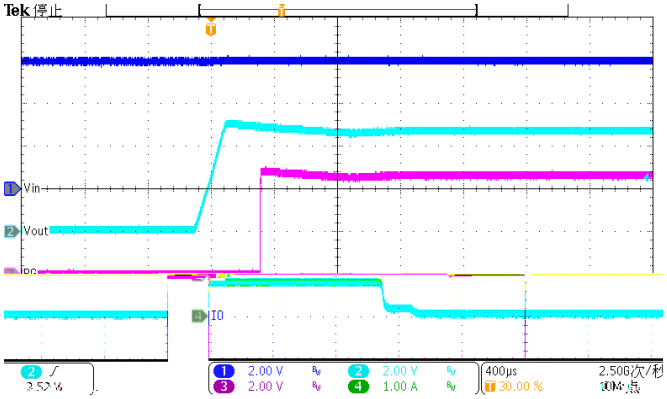


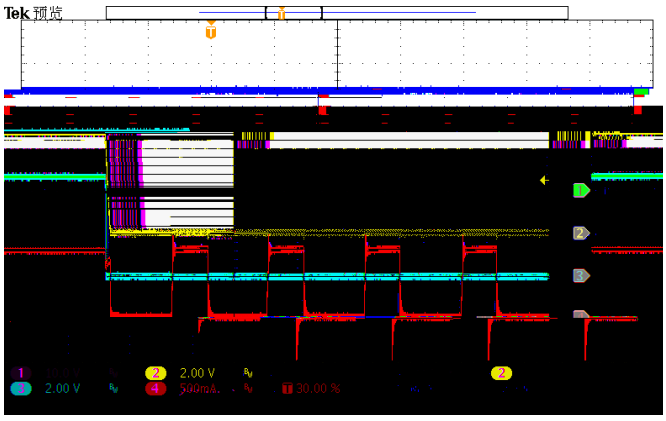
Application Waveforms(Continued)

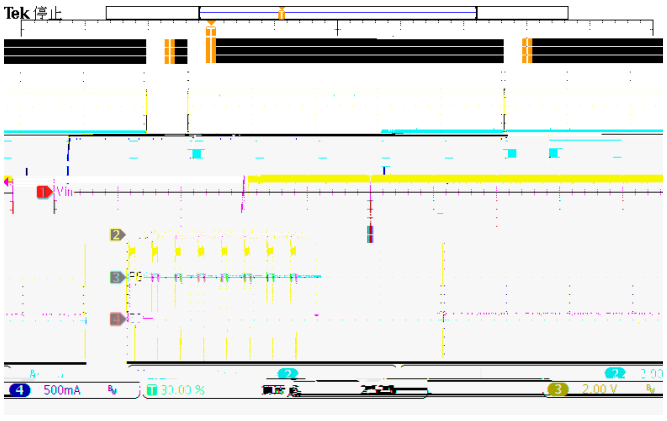






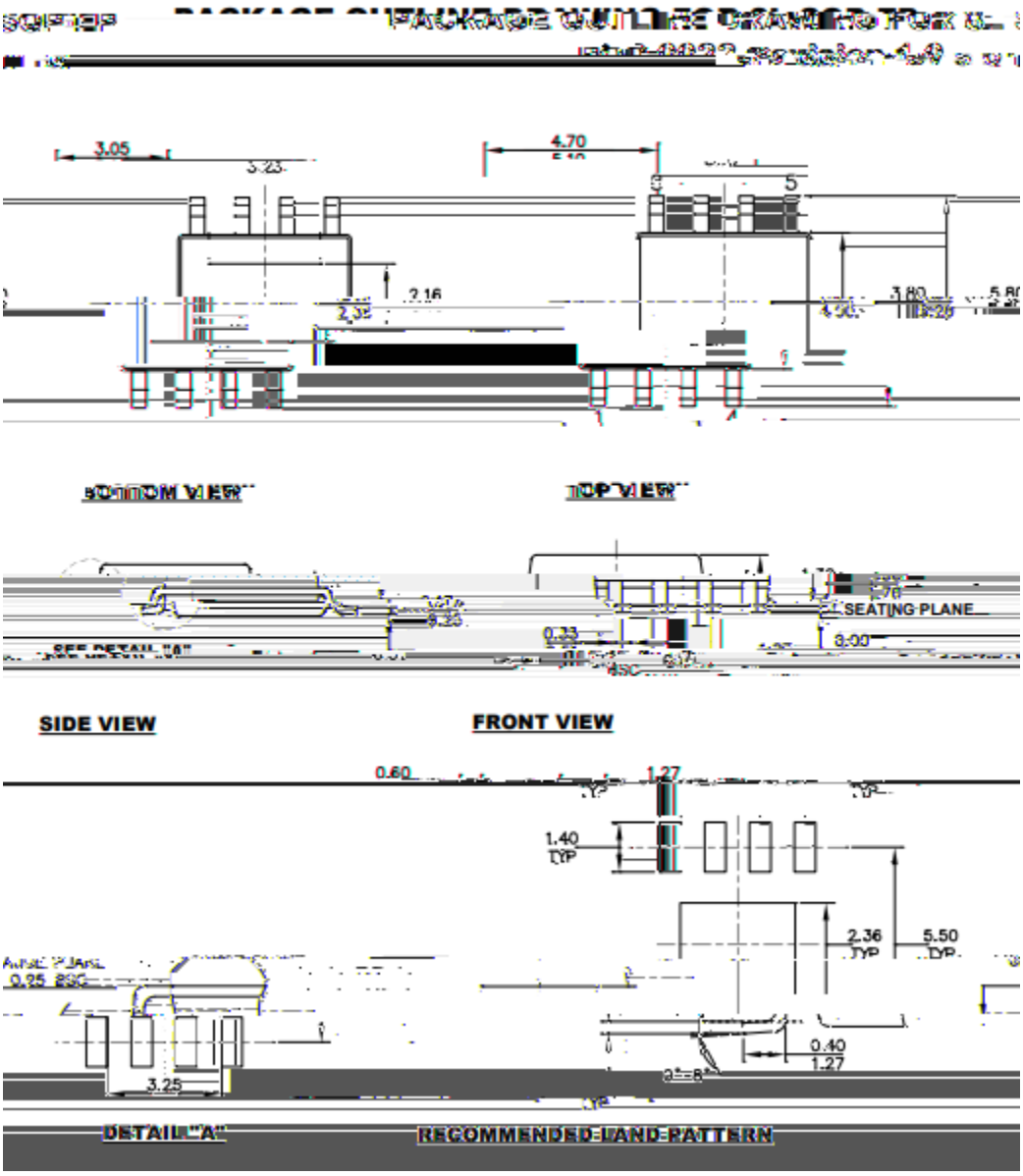






Layout Guideline

1.
2.


3.05
4.70
2.16
SEATING PLANE
SIDE VIEW
FRONT VIEW
0.60
1.27
1.40
TYP
2.36
TYP
5.50
TYP
0.40
1.27
3.25
DETAIL "A"
RECOMMENDED LAND PATTERN

NOTE:

1.
2.
3.
4.
5.
6.